

# ON-CHIP ESD SOLUTIONS FOR 5V TO 100V INTERFACES



**PowerQubic**<sup>®</sup>  
SOFICS

IMPROVES IC/SOC PERFORMANCE  
MEETS TOUGH ESD SPECIFICATIONS  
LOW-COST SOLUTION



# DESIGN WITHOUT CONSTRAINTS

Customizable protection: easily adapt for a broad set of reliability and functional requirements

Protect against most severe specifications like IEC 61000-4-2, ISO 7637-2, transient latch-up

## REDUCE TIME TO MARKET

Customized EOS/ESD solutions can be delivered within a few days for TSMC 0.25um and 0.18um BCD technologies

High Voltage concepts proven in and portable to advanced CMOS (65nm, 28nm)

Proven in more than 20 projects and applications

## STRONGLY REDUCE IC COST

Silicon and product proven ESD solutions help to reduce IC development cost

Smaller silicon footprint help to reduce the manufacturing cost by 1% to 10%

Compatible with the standard CMOS/BCD process flow



CONTACT US FOR MORE INFORMATION



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