

For media information contact:

Pieter Donck

+32-9-21.68.341

mailto:press@sarnoffeurope.com

Sarnoff Europe Announces Silicon Proven ESD Cells for UMC 65nm, 0.13um CMOS and 0.18um HV CMOS Under New Low-cost Business Model

GISTEL, BELGIUM (March 23, 2009) – Sarnoff Europe (www.sarnoffeurope.com) today announced that it has reached an important milestone in its intellectual property alliance with UMC. The TakeCharge® on-chip ESD protection solutions are now silicon proven in three UMC CMOS technologies.

Sarnoff's customized ESD solutions are available for UMC-produced 130nm CMOS, 180nm High Voltage CMOS and 65nm CMOS chips. The solutions are all complementary to UMC foundry rules and help customers design custom I/O and interface designs when standard available solutions cannot be used.

Sarnoff ESD technology offers customers easy, low threshold access to silicon-proven design solutions for custom requirements, including low capacitance, low leakage, small area, and high ESD performance. Customers can cost-effectively buy or license the specific cell they need to complete the ESD protection of their IC.

“With the addition of Sarnoff Europe's TakeCharge on-chip ESD protection design solutions to our IP Alliance Program, UMC customers will have easy access to superior ESD design solutions from an industry wide recognized leader in its field” said Ken Liou, director of the IP and Design Support Division at UMC.

Sarnoff Europe provides ESD solutions that are complementary to public, foundry or partner-owned solutions. Sarnoff Europe's solutions focus on high ESD performance (e.g. IEC-61000-4-2 level 4) for harsh IC environments, or advanced low capacitive (<100fF) clamps for high speed SerDes or RF applications, or low leakage (< 1nA) approaches for mobile or green applications. These silicon proven solutions are available for various process nodes and foundries under the TakeCharge brand name.

“Our alliance with UMC means that customers will achieve shorter design cycles for proven specialty solutions optimized with advanced ESD protection,” said Koen Verhaege, executive director of Sarnoff Europe. “Together, Sarnoff and UMC can better meet the specialized needs of our customers by bringing cost efficient, higher-quality solutions to market much faster.”

The complete list of available ESD protection solutions for UMC's CMOS technologies is available online at: <http://ipmaster.umc.com/IPCatalog/> ('Specialty IO' / 'ESD' category).

Sarnoff's ESD design solutions are product proven in more than 500 commercially released IC's in nano, standard and high voltage (HV) CMOS, BiCMOS and Bipolar CMOS DMOS (BCD) processes.

Sarnoff's TakeCharge product proven ESD protection is licensed for a broad spectrum of applications such as ASIC's, FPGA's, LCD drivers, power, consumer and communications IC's. TakeCharge customers include leading IDM, fabless and foundry IC producers worldwide, including Toshiba, Altera, Infineon, ST, Ricoh, OKI, Sony, Fujitsu, Renesas, Panasonic, Seiko Epson, NJR, PMC-Sierra, ON, AMIS, RedMere, Nanotech, Silicon Image, Gennum, Actel, CamSemi, THine, Inphi, Energy Micro, Inside Contactless, Cissoid, Tower and others.

About Sarnoff Corporation

Sarnoff Corporation (www.sarnoff.com) delivers vision, video and semiconductor technology innovations that empower government and commercial clients to see/sense, understand and control complex environments. Founded in 1942 as RCA Laboratories, Sarnoff makes continuous breakthroughs in real-time video processing for defense, security and surveillance; ICs, lasers, imaging and sensing devices; end-to-end video solutions. Sarnoff is a subsidiary of SRI International.

About Sarnoff Europe

Sarnoff Europe bvba (www.sarnoffeurope.com) headquartered in Gistel, Belgium, is a subsidiary company of Sarnoff Corporation. Sarnoff Europe assumes worldwide responsibility for the development and commercialization of Sarnoff's TakeCharge® on-chip ESD protection IP.